

ABSTRACT

This invention relates to an electrical device for self-clocked controlled pseudo random noise (PN) sequence
5 generation and comprising a plurality of sequence generation means adapted to:

- output a plurality of sequence values (Z_t) on the basis of a plurality of clock values (C_t),

wherein said electrical device further comprises:

- 10 • step pattern generation means (202) adapted to select a step pattern, comprising said plurality of clock values (C_t), from a plurality of possible step patterns on the basis of a step pattern select signal (W_t).

15 Hereby, a flexible and efficient self-clocked controlled pseudo random noise (PN) sequence generation is obtained.

This invention also relates to a method of self-clocked controlled pseudo random noise (PN) sequence generation.

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Figure 2 should be published.